ES1385.1 Resistor Cascade Board

User's Guide

## Copyright

The data in this document may not be altered or amended without special notification from ETAS GmbH. ETAS GmbH undertakes no further obligation in relation to this document. The software described in it can only be used if the customer is in possession of a general license agreement or single license. Using and copying is only allowed in concurrence with the specifications stipulated in the contract.

Under no circumstances may any part of this document be copied, reproduced, transmitted, stored in a retrieval system or translated into another language without the express written permission of ETAS GmbH.

© Copyright 2005 ETAS GmbH, Stuttgart

The names and designations used in this document are trademarks or brands belonging to the respective owners.

R1.0.1 EN - 04.2005

TTN F 00K 104 265

# Contents

1	Introd	duction		5
	1.1	Features	and Applications	6
	1.2	Block Dia	gram	8
2	Hard	ware Featu	res	9
	2.1	Outputs		9
		2.1.1	Accuracy	0
		2.1.2	Switching Times	0
		2.1.3	Overcurrent Protection	0
		2.1.4	Overvoltage	0
	2.2	VMEbus	Interface	1
		2.2.1	Backplane Connector J11	1
		2.2.2	Address Switches SW1 and SW2 1	1
3	Pin A	ssignment	s and Indicators	5
	3.1		0R 0-5" Outputs	
	3.2	LED		6

4	Technical Data	17
5	ETAS Contact Addresses	19
	Index	21

## 1 Introduction

This User's Guide contains a description of the ES1385.1 Resistor Cascade Board.

It consists of the following chapters:

• "Features and Applications" on page 6

The Introduction (this chapter) contains an overview of the properties and functions of the ES1385.1 Resistor Cascade Board.

• "Hardware Features" on page 9

This is where the properties of the ES1385.1 Resistor Cascade Board are described.

• "Pin Assignments and Indicators" on page 15

This chapter contains a description of the front-facing connector as well as information on what the LED display means.

• "Technical Data" on page 17

This section contains the technical data of the ES1385.1 Resistor Cascade Board.

#### note

Some components of the ES1385.1 Resistor Cascade Board may be damaged or destroyed by electrostatic discharges. Please keep the board in its storage package until it is installed.

The ES1385.1 Resistor Cascade Board should only be taken from its package, configured and installed at a working place that is protected against static discharge.

#### note

Watch out for protruding components when installing/uninstalling the ES1385.1 Resistor Cascade Board!

#### 1.1 Features and Applications

The ES1385.1 Resistor Cascade Board acts as a resistor cascade with six independent channels in VMEbus systems. Each channel consists of a series connection of 16 resistors – a relay (PhotoMOS) is connected parallel to every resistor.

Four channels have one cascade each with the following resistance values:

10 Ω - 20 Ω - 20 Ω - 40 Ω - 100 Ω - 200 Ω - 200 Ω - 400 Ω - 1 kΩ - 2 kΩ
 - 2 kΩ - 4 kΩ - 10 kΩ - 20 kΩ - 40 kΩ - 1 MΩ.

These channels are particularly used to simulate the internal resistance of oxygen sensors – the 1  $\,$  M  $\!\Omega$  resistor is intended to represent a cold oxygen sensor.

Resistances of up to 79.99 k $\Omega$  ( + 1  $\,$  M $\Omega)$  can be defined with this kind of cascade.

Two channels have one cascade each with the following resistance values:

•  $5 \Omega - 10 \Omega - 20 \Omega - 20 \Omega - 40 \Omega - 100 \Omega - 200 \Omega - 200 \Omega - 400 \Omega - 1 k\Omega - 2 k\Omega - 2 k\Omega - 4 k\Omega - 10 k\Omega - 20 k\Omega - 40 k\Omega$ 

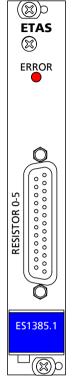
The additional 5  $\Omega$  resistor makes 5  $\Omega$  steps up to 79.995 k $\Omega$  possible – this makes it possible to simulate temperature sensors, for example.

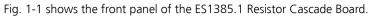
#### note

Every channel has a basic resistance of 20  $\Omega$  This is also the smallest resistance that can be set.

The ES1385.1 Resistor Cascade Board has the following properties:

- Six independent resistor cascades (channels)
- The accuracy of the resistances set is 5  $\Omega$  for channels 0 and 1 and 10  $\Omega$  for channels 2 5.
- The real values of the individual resistors of each cascade are stored as calibration data in the board's ROM. The monotony of the resistance values set is ensured by an algorithm implemented in the board's RTIO driver.
- The maximum operating voltage is 36 V.
- The maximum operating current over a cascade is 100 mA the current is monitored by a protective circuit.
- The overcurrent condition is reset via the RTIO.
- A newly set resistance is stable within 1 ms there may, however, be states of higher impedance during switching as the PhotoMOSs open within 0.2 ms.







For more details on the pin assignment of the "RESISTOR 0-5" connector, please consult section 3.1 on page 15; the meaning of the "ERROR" LED is described in section 3.2 on page 16.

## 1.2 Block Diagram

Fig. 1-2 shows a block diagram with all important functional units of the ES1385.1 Resistor Cascade Board.

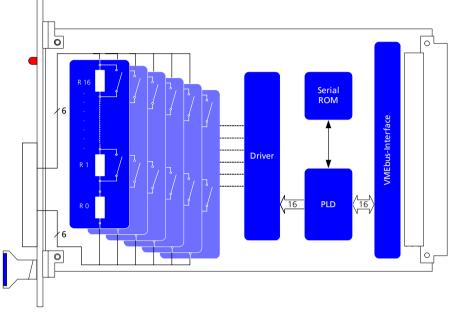


Fig. 1-2 Block Diagram of the ES1385.1 Resistor Cascade Board

## 2 Hardware Features

This section contains a description of the different hardware features of the ES1385.1 Resistor Cascade Board.

These are:

- "Outputs" on page 9
  - "Accuracy" on page 10
  - "Switching Times" on page 10
  - "Overcurrent Protection" on page 10
  - "Overvoltage" on page 10
- "VMEbus Interface" on page 11
  - "Backplane Connector J1" on page 11
  - "Address Switches SW1 and SW2" on page 11

#### 2.1 Outputs

Four channels of the ES1385.1 Resistor Cascade Board each have a cascade with the following resistance values:

- 10 Ω 20 Ω 20 Ω 40 Ω
- 100  $\Omega$  200  $\Omega$  200  $\Omega$  400  $\Omega$
- 1 kΩ 2 kΩ 2 kΩ 4 kΩ
- 10 kΩ 20 kΩ 40 kΩ
- 1 MΩ.

These channels are particularly used to simulate the internal resistance of oxygen sensors – the 1  $\;M\Omega$  resistor is intended to represent a cold oxygen sensor.

Resistances of up to 79.99 k $\Omega$  can be defined with this kind of cascade.

Two channels of the ES1385.1 Resistor Cascade Board each have a cascade with the following resistance values:

- 5Ω
- 10 Ω 20 Ω -20 Ω 40 Ω
- $100 \Omega 200 \Omega 200 \Omega 400 \Omega$
- 1 kΩ 2 kΩ 2 kΩ 4 kΩ
- 10 kΩ 20 kΩ 40 kΩ

The additional 5  $\Omega$  resistor makes 5  $\Omega$  steps to 79.995 k $\Omega$  possible. This kind of cascade can be used, for example, to simulate temperature sensors.

There is a relay (PhotoMOS) parallel to each resistor of a cascade which is used to activate the individual resistors.

#### note

Every channel has a basic resistance of 20  $\varOmega$  This is also the smallest resistance that can be set.

#### 2.1.1 Accuracy

The accuracy of the resistances set is 5  $\Omega$  for channels 0 and 1 and 10  $\Omega$  for channels 2 - 5. The real values of the individual resistors of each cascade are stored as calibration data in the board's ROM. The monotony of the resistance values set is ensured by an algorithm implemented in the board's RTIO driver.

#### 2.1.2 Switching Times

The various switching times when enabling and disabling the individual resistors of a cascade ( $t_{R_on} = 1 \text{ ms}$ ,  $t_{R_off} = 0.2 \text{ ms}$ ) can result in interim states with a high impedance ( $1 \text{ M}\Omega \text{ max.}$ ) – the value is stable, however, after just one millisecond.

#### 2.1.3 Overcurrent Protection

The ES1385.1 Resistor Cascade Board has overcurrent monitoring for every individual cascade. If a cascade's current exceeds 100 mA, the connection of the particular resistor cascade is interrupted. An overcurrent condition is shown by the red LED on the front panel (see "LED" on page 16).

The connection is not reestablished automatically after an interrupt caused by an overcurrent condition, but manually via the RTIO. The corresponding channel has to be "reenabled" for this purpose.

#### 2.1.4 Overvoltage

The ES1385.1 Resistor Cascade Board has no special overvoltage protection – please observe the maximum permissible voltage of 36 V.

#### 2.2 VMEbus Interface

#### 2.2.1 Backplane Connector J1

The assignment of the backplane connector J1 adheres to the VMEbus specification. For a detailed description, please refer to the ES4100 VME64x Signal Box User's Guide.

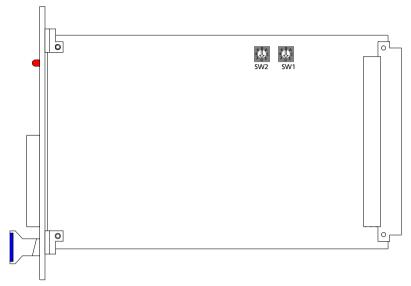
2.2.2 Address Switches SW1 and SW2

The ES1385.1 Resistor Cascade Board can be operated in both VMEbus and in VME64x systems with geographical addressing. If the SW1 and SW2 HEX switches are in position "0x00", the board is addressed in "geographical addressing mode" and otherwise in the relevant address spaces

The ES1385.1 maps 256 bytes into the A24 address space in the setting "0x00" depending on the slot position. Depending on the memory space available, the 64 kB address space is then defined dynamically by the system controller.

Slot Position	Address	VME Interface
		(Control Registers)
1	E0E000 - E0E0FF	256 bytes
2	E0E100 - E0E1FF	256 bytes
3	E0E200 - E0E2FF	256 bytes
4	E0E300 - E0E3FF	256 bytes
5	E0E400 - E0E4FF	256 bytes
6	E0E500 - E0E5FF	256 bytes
7	E0E600 - E0E6FF	256 bytes
8	E0E700 - E0E7FF	256 bytes
9	E0E800 - E0E8FF	256 bytes
10	E0E900 - E0E9FF	256 bytes
11	E0EA00 - E0EAFF	256 bytes
12	EOEBOO - EOEBFF	256 bytes
13	E0EC00 - E0ECFF	256 bytes
14	E0ED00 - E0EDFF	256 bytes
15	EOEFOO - EOEFFF	256 bytes
16	EOFOOO - EOFOFF	256 bytes
17	EOF100 - EOF1FF	256 bytes
18	EOF200 - EOF2FF	256 bytes
19	EOF300 - EOF3FF	256 bytes
20	EOF400 - EOF4FF	256 bytes
21	EOF500 - EOF5FF	256 bytes

Tab. 2-1Slot Position and Address





In every other setting of the HEX switch ( $\neq$  0x00), the 64 kB address space is assigned statically.

Switch Position	Address Space	
0x01	010000 - 01FFFF	
0x02	020000 - 02FFFF	
0x03	030000 - 03FFFF	
OxFF	FF0000 - FFFFFF	

**Tab. 2-2**Setting of the Address Spaces

SW1	SW2
0x0 <b>n</b>	0x <b>n</b> 0
Address A16 - A19	Address A23 - A20

Tab. 2-3HEX Switches for Setting the Address

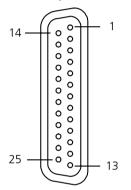
## 3 Pin Assignments and Indicators

This section contains a description of the pin assignments of the connector on the front panel and the meaning of the indicators on the front panel.

These are:

- "RESISTOR 0-5" Outputs" on page 15
- "LED" on page 16

Sub-D 25-pin, male.



 Tab. 3-1
 "RESISTOR 0-5" Connector (View from the Plug-In Side)

Pin	Signal	Pin	Signal
1	Res0.1	14	PE (housing)
2	Res0.2	15	n.c.
3	Res1.1	16	n.c.
4	Res1.2	17	n.c.
5	Res2.1	18	n.c.
6	Res2.2	19	n.c.
7	Res3.1	20	n.c.
8	Res3.2	21	n.c.
9	Res4.1	22	n.c.
10	Res4.2	23	n.c.
11	Res5.1	24	n.c.
12	Res5.2	25	n.c.
13	PE (housing)		

Tab. 3-2 "RESISTOR 0-5" Pin Assignment

## 3.2 LED

There is 1 LED on the front panel of the ES1385.1 Resistor Cascade Board, the meaning of which is described in Tab. 3-3.

LED	Color	Meaning
ERROR	Red	The overcurrent condition is active for at least one channel or SYSFAIL active

Tab. 3-3Significance of the LED

## 4 Technical Data

This chapter contains the technical data of the ES1385.1 Resistor Cascade Board.

Outputs

No. of channels	6
Resistance values	Four channels: 10 $\Omega$ - 20 $\Omega$ - 20 $\Omega$ - 40 $\Omega$ - 100 $\Omega$ - 200 $\Omega$ - 200 $\Omega$ - 400 $\Omega$ - 1 k $\Omega$ - 2 k $\Omega$ - 2 k $\Omega$ - 4 k $\Omega$ - 10 k $\Omega$ - 20 k $\Omega$ - 40 k $\Omega$ - 1 M $\Omega$ . Two channels: 5 $\Omega$ - 10 $\Omega$ - 20 $\Omega$ -20 $\Omega$ - 40 $\Omega$ - 100 $\Omega$ - 200 $\Omega$ - 200 $\Omega$ - 400 $\Omega$ - 1 k $\Omega$ - 2 k $\Omega$ - 2 k $\Omega$ - 4 k $\Omega$ - 10 k $\Omega$ - 20 k $\Omega$ - 40 k $\Omega$
Accuracy	Channels 0 - 1: 5 $\Omega$ Channels 2 - 5: 10 $\Omega$
Switching time	Resistance value stable within 1 ms
Maximum permissible voltage	36 V
Overcurrent fuses	Per channel: 100 mA
VME Conformity	
VME specification	Revision C.1, October 1985 and IEC 821-1987
Туре	Slave
Data bus	A24:D16
Address modifier	39 (hex): A24 non-privileged data access
Base address	\$000000-FF0000 jumper-programmable or by VME64x backplane slot detection automatically
Memory map	Short I/O space, occupying 64 kB
Interrupts	Single level, IRQ 1 – 7 By software: – IRQ level – interrupt vector source

Power Supply

Current consumption

0.5 A @ +5 V DC

Environmental Conditions

Operating temperature	5 °C to 35 °C (41 °F to 95 °F)
Relative humidity	0 to 95% (non-condensing)

Physical Dimensions

Printed circuit board (L x W)	160 mm x 100 mm
Front panel	Height: 3 U Width: 4 HP

## 5 ETAS Contact Addresses

ETAS HQ		
ETAS GmbH		
Borsigstr. 14	Phone:	+49 (711) 8 96 61-0
70469 Stuttgart	Fax:	+49 (711) 8 96 61-105
Germany	E-mail:	sales@etas.de
	WWW:	www.etasgroup.com
France		
ETAS SAS		
1, place des Etats-Unis	Phone:	+33 (1) 56 70 00 50
SILIC 307	Fax:	+33 (1) 56 70 00 51
94588 Rungis Cedex	E-mail:	sales@etas.fr
France	WWW:	www.etasgroup.com
Great Britain		
ETAS Ltd.		
Studio 3, Waterside Court	Phone:	+44 (0) 1283 - 546512
Third Avenue, Centrum 100	Fax:	+44 (0) 1283 - 548767
Burton-upon-Trent	E-mail:	sales@etas-uk.net
Staffordshire DE14 2WQ	WWW:	www.etasgroup.com
England		
Japan		
ETAS K.K.		
Queen's Tower C-17F 2-3-5, Minatomirai,	Phone:	+81 (45) 222-0900
Nishi-ku	Fax:	+81 (45) 222-0956
Yokohama 220-6217	E-mail:	sales@etas.co.jp
Japan	WWW:	www.etasgroup.com

#### Korea

ETAS	Korea	Co.	Ltd.
------	-------	-----	------

ETAS Inc.		
North America		
Republic of Korea		
Seoul	WWW:	www.etasgroup.com
Seocho-gu	E-mail:	sales@etas.co.kr
61-1, Yangjae-dong	Fax:	+82 (2) 5747 120
3F, Samseung Bldg.	Phone:	+82 (2) 5747 016

3021 Miller Road	Phone:	+1 (888) ETAS INC
Ann Arbor, MI 48103	Fax:	+1 (734) 997-9449
USA	E-mail:	sales@etas.us
	WWW:	www.etasgroup.com

## Index

## Α

Accuracy 10 Address switches SW1, SW2 11 Application 6

## В

Backplane connector J1 11 Block diagram 8

**F** Front panel 7

**H** Hardware features 9

## L

Introduction 5

## L

LED 16

## 0

Outputs 9 Overcurrent protection 10 Overvoltage 10

### Ρ

Pin assignment RESISTOR 0-5 15

**S** Switching times 10

#### **T** Technical data 17

#### v

VMEbus interface 11 backplane connector J1 11