![ETAS logo] **ETAS**
DRIVING EMBEDDED EXCELLENCE

Question:

How to check the angle clock bus connection between ES5370.1 Carrier Board and ES5340.1 / ES5340.

In case of incorrect Angle Clock Bus Connection between ES5370.1 Carrier Board and ES5335 / ES5340.1 / ES5340.2 there will be no correct behaviour. This is also valid for a single ES5340 RPM Master.
In this case no angle synchronous trigger and clock signals with ES5340-RPM are created
(exception Local Master usage (GLOBALS-RPM Operating Mode = Master), Sync Bus Resource: None).

Answer:

If fitting the ES5370.1 Carrier Board with an ES5340.1 or an ES5340.2 the boards must be connected with the angle clock bus ribbon cable as described in the related ES5300.1-A - UsersGuide.pdf (page 40).

The correct connection can easily be checked with e.g. LABCAR-RTPC V6.3.7 as follows:

Checking if SYNC Bus is "clean":
1. Open http://192.168.40.14/cgi-bin/labcar-hardware-boards
2. Open "**Detected ETAS LABCAR PCIe Boards**"
3. Open "**Detailed ETAS LABCAR PCIe Board Information**"

```
▼ Detailed ETAS LABCAR PCIe Board Information

es5350     0 250:6 16f2:0200 0000:2b:00.0 0:4(-) 1:10 "ready and up", T=28.00C, Fan OFF
es5350     1 250:5 16f2:0200 0000:22:00.0 0:5(-) 1:12 "ready and up", T=28.00C, Fan OFF
es5321     1 250:4 16f2:0200 0000:20:00.0 0:3(-) 1:6  "ready and up", T=27.62C, Fan OFF
es5300bp   1 250:3 16f2:0200 0000:1b:00.0 0:2(-) -1:-1 "ready and up", T=28.87C, Fan OFF
es5340m    0 250:2 16f2:0200 0000:0e:00.0 1:0(-) 0:15 "ready and up", T=28.87C, Fan OFF
es5321     0 250:1 16f2:0200 0000:07:00.0 0:1(-) 0:4  "ready and up", T=27.12C, Fan OFF
es5300bp   0 250:0 16f2:0200 0000:04:00.0 0:0(-) -1:-1 "ready and up", T=28.50C, Fan OFF

/proc/es53xx/sync_0:
sync #0: clean
     bit #    { 0 1 2 3 4 5 6 7 }
--------------------------------------

  es5300bp_0  { x x x x x x x x }
              | | | | | | | |
    es5321_0  { x x x x x x x x }
              | | | | | | | |
  es5300bp_1  { x x x x x x x x }
              | | | | | | | |
    es5321_1  { x x x x x x x x }
              | | | | | | | |
    es5350_0  { x x x x x x x x }
              | | | | | | | |
    es5350_1  { x x x x x x x x }



/proc/es53xx/sync_1:
sync #1: clean
     bit #    { 0 1 2 3 4 5 6 7 }
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    es5340m_0  { x x x x x x x x }
```

4. sync #0: clean -> ok
   sync #0: tainted -> error

In case of further questions:

Please feel free to contact our Support Center, if you have further questions.
Here you can find all information: http://www.etas.com/en/hotlines.php?langS=true&

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