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Product :	ETK-S9.0	ETK-S9.0			
Title :	Change Informa	ation			
File :	ETK-S9.0_Chan	ge_Information_V01.	doc		
TTNr :	F-00K-107-066	F-00K-107-066			
Comments :	Currently shipped: 334040A010 EPLD version: V33 FPGA-Boot version: V40 FPGA-A version: V40 Hardware-state: A010				
Created:	Name Department Signature Date Mai MCD/PRM-H Mai 10.02.2011				
Released:	Name Laichinger	Department EPH	Signature Laichinger	Date 14.02.2011	

Changes

Revision	Description	Date	Name	Signature
01	334040A010 for ETK-S9.0A - Initial version	10.02.2011	Mai	Mai



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1 General remarks to this document

This document consists of three main parts.

Chapter 2 contains general information about the required tool-chain to use these ETKs. Three different items are described.

- Explanation of the version-system of the ETK-S9.0
- Description of the different product variants
- The required versions of software (INCA / ASCET), HSP (calibration hardware firmware of e.g. ES59x, ES690, ES1232, ...) and ETK - hardware.
 Additionally other requirements for running the ETK.

Chapter 3 contains information about PLD-Code changes concerning these ETKs.

Chapter 4 contains information about hardware changes concerning these ETKs.



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2 Version description and Tool-Chain Information

2.1 Version-Syntax of the ETK-S9.0

The ETK-S9.0 version information is located on the sticker of the ETK or can be read out of the ETK using the ETK-Configuration Tool.

The version information has the following syntax: aabbccdeee/ff

PLD-Code Information:

aa: EPLD-Code version (10, 11, 12...) see chapter 3 **bb:** FPGA-Boot-Code version (10, 11, 12...) see chapter 3 **cc:** FPGA-A-Code version (10, 11, 12...) see chapter 3

Hardware-Information:

d: PCB version (A, B, C, ...)

eee: Hardware state of the PCB (010, 011, 012, ...) see chapter 4

ff: Assembly variant of the PCB (00, 01, 02, ...)

The first delivered hardware states of the ETK-S9.0 were the following:

- ETK-S9.0A: **334040A010/01**



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2.2 Mechanical dimensions

ETK-S9.0A

Item number	F-00K-107-066				
Description	Emulator Probe for Fi ECU adaption via 26			cessor,	
For details refer to datasheet	4* ØH				G F E O
	D	IM	MILLIMETERS	INCHES	
		A	60.00+0.2	$2.362^{+0.008}_{-0.008}$	
		В	56.50 ^{+0.1}	$2.224^{+0.004}_{-0.004}$	
		C	33.00+0.2	1.299+0.008	
		D	3.50+0.1	0.138+0.004	
		E	3.00+0.1	0.118+0.004	
	<u> </u>	F	37.00 ^{+0.1}	1.457+0.004	
		G	40.00+0.2	1.575+0.008	
		Н	2.60+0.1	$0.102^{+0.004}_{-0.000}$	



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2.3 Version information of the Tool-Chain components

To get this ETK running with the other components of the Tool-Chain please make sure that the version mentioned below or a newer one is used. If your software-, firmware- or hardware version is older, please update it. If you have any problems to get this ETK running please contact our local customer support or sales representative.

The registered users with a valid INCA SW Service Contract will automatically receive the newest INCA-version (CD-ROM).

Updates or refreshes can also be downloaded from the ETAS homepage:

www.etas.com

2.3.1 Hardware support

The ETK-S9.0 is supported by ES690, ES59x, ES910 and ES100.2/3 system with ES1232.

2.3.2 Software and microcontroller support

List of supported Infineon microcontroller:

ETK-S9.0A

Microcontroller	HSP	INCA	ETK Drivers	ASCET-RP	INTECRIO
			and Tools		
MPC5674	V9.1.0	V6.2.1	V2.1.10	V6.1.0	V3.2.0



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3 PLD-Code Changes

3.1 General remarks to this chapter

The programmable logic code within the ETK-S9.0 is stored onto programmable logic devices (FPGA and EPLD). For the version syntax please refer to chapter 2.1.

3.2 ETK-S9.0A

3.2.1 EPLD-Code

Revision	File date	Description
Version 3.3	22.12.2010	Initial Version

Delivery condition:

The EPLD version 3.3 will be programmed into all shipments.

3.2.2 FPGA-Boot-Code

Revision	File date	Description
Version 4.0	22.12.2010	Initial Version

Delivery condition:

The FPGA-Boot version 4.0 will be programmed into all new shipments.

3.2.3 FPGA-A-Code

Revision	File date	Description
Version 4.0	22.12.2010	Initial Version

Delivery condition:

The FPGA A version 4.0 will be programmed into all new shipments.



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4 Hardware Changes

4.1 General remarks to this chapter

Hardware problems or obsolete parts can make it necessary to change the manufacturing of this ETK. Information about the changes is listed underneath. For the version syntax please refer to chapter 2.1.

4.2 ETK-S9.0A Changes

Hardware-state	Description
A010	Initial Version

Delivery condition

The hardware-state A010 will be delivered with all new shipments.



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5 Abbreviations

ETK	Emulator test probe	
ES1000	VME - system, successor of INCA-VME	
INCA-VME	Old VME - system for MC and RP	
ES690	MC hardware, successor of MAC2	
ES59x	MC hardware, successor of ES690	
MAC2	Old MC hardware	
INCA	MC software, successor of VS100	
VS100	MC software	
ETK Configuration Tool	Configuration Software, in order to configure an ETK,	
	successor of the old DOS tool	
DOS ETK-Config-Tool	Old configuration software, in order to configure an ETK	
HSP	Hardware Service Pack; ETAS product which includes the	
	firmware for the complete ETAS hardware, shipped	
	together with INCA but also available as standalone	
	product, download at ETAS homepage possible	
Firmware	Software for MC hardware; necessary for implementation	
	of new features or bugfixes	
Hotfix	Software bugfix for a refresh version	
Tool-chain	MC hardware (e.g. ES690) and software (e.g. INCA)	
MC	Measurement & Calibration	
RP	Rapid Prototyping	
PLD	Programmable Logic Device	
FPGA	Free Programmable Gate Array; interface component to	
	the application hardware	
PCB	Printed Circuit Board	
DPR	Dual Ported RAM; special RAM onto the ETK which allows	
	an access from ECU and application hardware at the	
	same time	
/CS	Chip select	