Product :	ETKT2.1			
Title :	Change Information			
File :	ETKT2.1_Change_Information_V03.doc			
TTNR :	ETK-T2.1A F-00K-106-042 ETK-T2.1B F-00K-106-043 ETK-T2.1C F-00K-106-044 ETK-T2.1D F-00K-106-640			
Comments :	Currently shipped: **23A010** FPGA version: V2.3 Hardware-state: A010			
Created:	Name Mai	Department PGA/PRM-H	Signature Mai	Date 2011-07-20
Released:	Name Dudziak	Department PJjM	Signature Dudziak	Date 2011-07-21

Changes

Revision	Description	Date	Name	Signature
01	20A010 - initial Version	2008-02-11	Mai	Mai
02	20A010 – HW Revision ETK-T2.1D added [chapter 4.3]	2009-07-17	Mai	Mai
03	23A010 - New FPGA Version [chapter 3.2]	2011-07-20	Mai	Mai

Table of Contents

1 General remarks to this document

This document consists of three main parts.

Chapter 2 contains general information about the required tool-chain to use this ETK.
Two different items are described.

- Explanation of the version-system of the ETKT2.0
- The required versions of software (INCA / ASCET) , HSP (calibration hardware firmware of e.g. ES690, ES1232, ..) and ETK - hardware.
  Additionally other requirements for running the ETK.

Chapter 3 contains information about PLD-Code changes concerning this ETK

Chapter 4 contains information about hardware changes concerning this ETK

2 Tool-Chain Information

2.1 Version-Syntax of the ETKT2.1

The ETK-T2.1 version information is located on the sticker of the ETK or can be read out of the ETK using the ETK-Configuration Tool.

The version information has the following syntax: **aacddd/ee**

PLD-Code Information:
aa: FPGA-Code version (10, 11, 12,...) see chapter 3

Hardware-Information:
c : PCB version (A, B, C, ...)
ddd: Hardware state of the PCB (010, 011, 012, ...) see chapter 4
ee: Assembly variant of the PCB (00, 01, 02, ...)

The first delivered hardware states of the ETK-T2.1 were the following.

- ETK-T2.1A: **20A010/01**
- ETK-T2.1B: **20A010/02**
- ETK-T2.1C: **20A010/03**

2.2 Version information of the Tool-Chain components

To get this ETK running with the other components of the Tool-Chain please make sure that the version mentioned below or a newer one is used. If your software-, firmware- or hardware version is older, please update it.
If you have any problems to get this ETK running please contact our local customer support or sales representative.

Hardware

Name	**Needed version**	**Remarks**
ES690	V 10.0.0	Supported
ES590/ES591	V 10.0.0	Supported
ES910	V 1.1.4	Supported
ES1000.2/3 - system	V 10.0.0	ES1232 supported with ES1120
ES1000.1	Not supported	
MAC2	Not supported	

Software:

List of supported Infineon microcontroller:

Microcontroller	HSP	INCA	ETK Drivers and Tools	ASCET-RP	INTECRIO
TC1792	V5.0	V5.4	n. a.	V5.4	V1.1
TC1796	V5.0	V5.4	n. a.	V5.4	V1.1
TC1796ED	V5.0	V5.4	n. a.	V5.4	V1.1
TC1797 [1)]	V5.0	V5.4	n. a.	V5.4	V1.1
TC1797 [2)]	V5.0	V6.1	V1.1.2	V5.4	V1.1
TC1797ED [1)]	V5.0	V5.4	n. a.	V5.4	V1.1
TC1797ED [2)]	V5.0	V6.1	V1.1.2	V5.4	V1.1

1) Operating measurement and calibration, no Braindead Flashing
2) Operating measurement and calibration, and Braindead Flashing

The registered user will automatically receive the newest INCA-version (CD-ROM)
Updates or refreshes can be downloaded from the ETAS homepage:
http://de.etasgroup.com
http://en.etasgroup.com

3 PLD-Code Changes

3.1 General remarks to this chapter

The programmable logic code within the ETKT2.1 is stored onto programmable logic devices (FPGA). The FPGA-code starts with version 2.0. For the version syntax please refer to chapter 2.1.

3.2 FPGA-Code version 2.0 was the first delivered version

3.2.1 Details of change

Version 2.3: File dated 12.10.2010
Cause: Parallel writing of Reference and Working page for Service Based Bypass V3 was added

Attention: For updating the ETK - HDC with a later version by using the ETK - configuration tool, all ETK - FPGA - packages will be updated one after another and will last a few minutes.

3.2.2 Delivery condition

The FPGA version **2.3** will be programmed into all shipments.

4 Hardware Changes

4.1 General remarks to this chapter

Hardware problems or obsolete parts can make it necessary to change the manufacturing of this ETK. Information about the changes are listed underneath. The hardware-state starts with version A010. For the version syntax please refer to chapter 2.1.

4.2 No changes at hardware-state A010

4.2.1 Delivery Condition

The hardware-state **A010** will be delivered with all shipments.

4.3 Assembly-Variant

Assembly-Variant	
Variant	Description
01	ETK-T2.1A (without BDR),
02	ETK-T2.1B (with BDR)
03	ETK-T2.1C (with BDR and mounting holes)
04	ETK-T2.1D (without BDR and soldered TC1797ED, instead of socket)

Abbreviations

ETK	Emulator test probe
ES1000	VME - system, successor of INCA-VME
INCA-VME	Old VME - system for MC and RP
ES690	MC hardware, successor of MAC2
MAC2	Old MC hardware
INCA	MC software, successor of VS100
ETK Configuration Tool	Configuration Software, in order to configure an ETK, successor of the old DOS tool
HSP	**H**ardware **S**ervice **P**ack; ETAS product which includes the firmware for the complete ETAS hardware, shipped together with INCA but also available as standalone product, download at ETAS homepage possible
Firmware	Software for MC hardware; necessary for implementation of new features or bugfixes
Hotfix	Software bugfix for a refresh version
Tool-chain	MC hardware (e.g. ES690) and software (e.g. INCA)
MC	**M**easurement & **C**alibration
RP	**R**apid **P**rototyping
PLD	**P**rogrammable **L**ogic **D**evice
FPGA	**F**ield **P**rogrammable **G**ate **A**rray; interface component to the application hardware
PCB	**P**rinted **C**ircuit **B**oard
DPR	Dual Ported RAM; special RAM onto the ETK which allows an access from ECU and application hardware at the same time
/CS	Chip select
BDR	Brain Dead Recovery