

Product:	XETK-T2.2	Rev :	02	Page 1 of 12		
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Product :	XETK-T2.2			
Title :	Change Informat	tion		
File:	XETK-T2.2_Chan	ge_Information_V02.do	C	
	XETK-T2.2A	F-00K-107-444		
TTND .	XETK-T2.2B	F-00K-107-445		
TTNR:	XETK-T2.2C	F-00K-107-446		
	XETK-T2.2D	F-00K-107-447		
Comments :		(hardware state): firmware version:	A010 HSP 9.9.2	
Created:	Name R. Mai	Department PGA/PRM-H	Signature Mir	Date 2013-02-01
Released:	Name A.Jaeckle	Department EHE1	Signature Jae	Date 2013-02-01

Changes

Revision	Description	Date	Name	Signature
01	A010 initial hardware version [chapter 5] HSP 9.5.0 initial firmware version [chapter 6]	2011-10-06	R. Mai	R. Mai
02	HSP 9.9.2 Solved Issues added [chapter 3] New Firmware Modifications added [chapter 6]	2013-02-01	R. Mai	R. Mai



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1 General Information

1.1 Safety Notice

Calibration activities influence the behavior of the ECU and the systems controlled by the ECU. This may result in unexpected behavior of the vehicle and thus can lead to safety critical situations. Only well trained personnel should be allowed to perform calibration activities.

1.2 System Requirements

Recommended system requirements on a tool PC for Measurement & Calibration:

2 GHz Pentium-PC or equivalent, equipped with

- 512 MB RAM (basic hardware), depending on the use cases 1GB RAM are advantageous
- Hard disk with minimum 10 GB free disk space
- CD-ROM for installation
- XGA-Graphic card with XGA-screen and resolution of 1024 x 768 with 16 bit colors
- Fast Ethernet adapter 100BaseT
 - with full duplex capability
 - configured as component TCP/IP only
 - separate to e.g. company network
- WINDOWS® XP SP2, WINDOWS® 2000 SP3 or higher, WINDOWS®VISTA

For the usage of INCA the user needs read and write access to the registry path HKEY_LOCAL_MACHINE\Software\ETAS.

1.3 Restrictions

WINDOWS® 95b, WINDOWS® NT and WINDOWS® 98SE are not supported

Depending on the used PC hardware and software, the PC performance and thus the performance of INCA might be negatively affected when hyper threading is activated. In such a case we recommend not to use hyper threading.

1.4 Miscellaneous

To ensure the highest data throughput from the XETK device up to the PC system no other PC software should be run via this Ethernet adapter.



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2 Version Syntax and Tool-Chain Information

2.1 Version-Syntax of the XETK-T2.2

The XETK-T2.2 hardware version information is located on the sticker of the XETK or can be read out of the XETK using the firmware update tool HSP.

Hardware State Syntax: abbb/cc

Description (modification details refer chapter 4)

a PCB Version (A=V1.0, B=V1.1, C=V1.2, ...) **bbb** PCB Hardware State (010, 011, 012, ...) **cc** PCB Population Variant (00, 01, 02, ...)

The XETK-T2.2 Firmware version information can be read out of the XETK using the firmware update tool HSP. It is not printed onto a XETK sticker.

Firmware-Version Syntax: aaa.bbb.cccc

Description (modification details refer chapter 5)

aaaMajor Release (0...255)bbbMinor Release (0...255)ccccRevision/Patch (0...65535)

Firmware Packages:

HDC Work aaa.bbb.ccccc
Firmware Work aaa.bbb.ccccc
HDC Rescue aaa.bbb.ccccc
Firmware Rescue aaa.bbb.ccccc
CPLD aaa.bbb.ccccc



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2.2 Version information of the tool-chain components

To get this XETK running with the other components of the tool-chain please make sure that the version mentioned below or a newer one is used. If your software-, firmware- or hardware version is older, please update it.

If you have any problems to get this XETK running please contact our local customer support or sales representative.

Product Name	Version	Remarks
Firmware / HSP	V9.5.0	Initial version
INCA	V6.2.1	Initial version
ETK Drivers and Tools	V2.1.13	Initial version for INCA 6.2.1
ETK Drivers and Tools	V3.4.0	Initial version for INCA 7.0
XETK Configuration Tool	V2.1.13	Initial version for INCA 6.2.1
XETK Configuration Tool	V3.4.0	Initial version for INCA 7.0
ASCET-RP	Not supported	
INTECRIO	V3.1.0	Initial Version
RTA-TRACE	V2.1.2	Initial Version

The registered user will automatically receive the newest INCA-version (CD-ROM) Updates or refreshes can be downloaded from the ETAS homepage:

http://de.etasgroup.com http://en.etasgroup.com



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3 What's New - Release Notes

This chapter lists the main improvements compared to a previous shipped XETK product. Additionally a detailed list of already known issues can be found here.

3.1 New or Enhanced Functions

3.1.1 In HSP V9.9.2

Туре	Description
NEW	MCE V2 support: Added multicast capability for measurement data
	transfer with XCP on UDP

3.2 Known issues

Currently none



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4 Product Variants

In general the XETK-T2.2 can be purchased in four variants. The electrical and system functionality differs a bit in the all variants (ECU Adapter and BDF support).

4.1 XETK-T2.2A

Item number	F-00K-107-444			
Description	Emulator probe for ECUs with Infineon m 1 MByte emulation RAM and a 441 pin EC without BDR support			179x with
For details refer the datasheet	A	_	A WILL WETERC	INQUEC
	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	DI		INCHES
		LA	72.00	2.835
		В	52.00	2.047
		С	37.00	1.457
) D	9.50	0.374
		Ε	35.00	1.378
		F	21.00	0.827
		G	18.50	0.728
	- - ^φ J	Н	31.00	1.220
		J	2.70	0.106

4.2 XETK-T2.2B

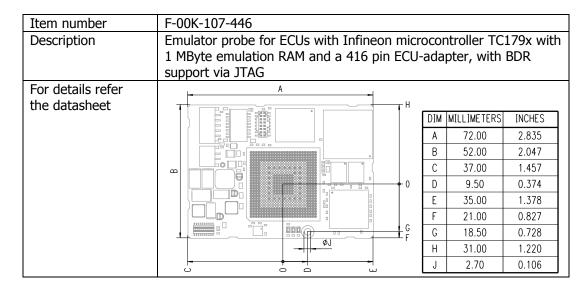
Item number	F-00K-107-445			
Description	Emulator probe for ECUs with Infineon mi 1 MByte emulation RAM and a 441 pin EC with BDR support via JTAG			179x with
For details refer the datasheet	A H	DIM A B C D E	72.00 52.00 37.00 9.50 35.00 21.00	INCHES 2.835 2.047 1.457 0.374 1.378 0.827
	O O O O	G H J	18.50 31.00 2.70	0.728 1.220 0.106



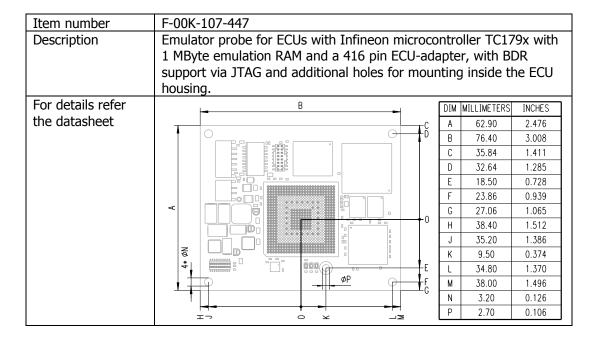
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4.3 XETK-T2.2C



4.4 XETK-T2.2D





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5 Hardware Modifications

5.1 General remarks to this chapter

Hardware issues or obsolete parts can make it necessary to modify the population of the XETK. Information about the modifications is listed underneath. The hardware state starts with version **A010**. For the version syntax please refer to chapter 2.1.

5.2 No modification at hardware state A010

5.3 Hardware delivery condition

The hardware state **A010** will be delivered with all new shipments.



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6 Firmware Modifications

6.1 General remarks to this chapter

The programmable logic code within the XETK-T2.2 is stored onto programmable logic devices (FPGA, CPLD). The first released firmware version is listed underneath. For the version syntax please refer to chapter 2.1.

6.2 First delivered version

FPGA Work	1.1.0
Firmware Work	1.0.31
FPGA Rescue	1.1.0
Firmware Rescue	1.0.31
CPLD	1.1.4

Attention:

For updating the XETK - firmware with a later version by using HSP, all XETK firmware packages will be updated one after another. This will last a few minutes and must not be cancelled by the user.

In case the firmware update had been finished unsuccessfully due to some reason, the update will have to be repeated. HSP will program the rescue packages onto the XETK. This procedure makes the firmware update fail-safe.

6.3 Firmware delivery condition

The following firmware versions will be programmed into all XETK shipments:

FPGA Work	1.1.0
Firmware Work	1.0.33
FPGA Rescue	1.1.0
Firmware Rescue	1.0.33
CPLD	1.1.4

In kind of any problems the above mentioned firmware can be programmed to the XETK by using HSP V9.9.2. This HSP version is similar to the currently delivered XETK products. Newer HSP versions could contain bug fixes and / or new features.

Attention: For updating the XETK - FPGA with a later version by using the

HSP Firmware update tool, all XETK - packages will be updated

one after another and will last a few minutes.



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7 Abbreviations

XETK	Product (emulator test probe) which can directly be connected to the tools PC
INCA	Measurement and Calibration Software of ETAS
ASCET-RP	Rapid Prototyping Software of ETAS
INTECRIO	Rapid Prototyping Software of ETAS
XETK Configuration Tool	Configuration Software, in order to configure a XETK
HSP	Hardware Service Pack; ETAS product which includes the
	firmware for the complete ETAS hardware, shipped
	together with INCA but also available as standalone
	product, download at ETAS homepage possible
firmware	Software for MC hardware; necessary for implementation
	of new features or bug fixes
Hot-fix	Software bug-fix for a refresh version
tool-chain	MC hardware (e.g. ES690) and software (e.g. INCA)
MC	Measurement & Calibration
RP	Rapid Prototyping
CPLD	Complex Programmable Logic Device
FPGA	Field Programmable Gate Array; interface component to
	the application hardware
PCB	Printed Circuit Board
DPR	Dual Ported RAM; special RAM onto the ETK which allows
	an access from ECU and application hardware at the
	same time
/CS	Chip select