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Title :	Release-Notes			



Product :	BR_XETK-S3.0A			
Title :	Release Notes			
File :	BR_XETK-S3_Release-Notes_V25.docx			
TTNR :	F-00K-110-751			
Comments :	Current shipped hardware state: E010/01 Current released firmware version: HSP 14.1.0			
Created:	Name R. Mai	Department ETAS-DAP/XPC-Fe1	Signature R. Mai	Date 2017-06-27
Released:	Name K.-H. Nirschl	Department ETAS-DAP/XPC-Fe3	Signature K.-H. Nirschl	Date 2024-06-11

Changes

Revision	Description	Date	Name	Signature
01	Initial Version	2017-06-27	Mai	Mai
02	Additional μ C support, Trace to RAM and IEEE1588, Buafixes	2017-09-13	Förmer	P. Förmer
03	Improvements and bug fixes with HSP11.7.0	2017-12-01	Mai	Mai
04	Additional μ C support, Improvements and bug fixes with HSP11.8.0	2018-03-13	Mai	Mai
05	Additional BIST support with HSP11.8.1	2018-04-10	Mai	Mai
06	Improvements and bug fixes with HSP11.9.0	2018-06-18	Mai	Mai
07	PCB Change from Rev.C to Rev.D	2018-07-23	Mai	Mai
08	Improvements and bug fixes with HSP11.10.0	2018-09-13	Mai	Mai
09	Improvements and bug fixes with HSP11.11.0	2018-12-12	Mai	Mai
10	Improvements and bug fixes with HSP11.12.0	2019-03-20	Mai	Mai
11	Improvements and bug fixes with HSP11.13.0	2019-06-06	Mai	Mai

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12	Improvements and bug fixes with HSP11.14.0	2019-09-17	Mai	Mai
13	Improvements and bug fixes with HSP11.15.0 and PCB Change from Rev.D to Rev.E	2019-12-10	Mai	Mai
14	Improvements and bug fixes with HSP12.0.0	2020-03-23	Mai	Mai
15	Improvements and bug fixes with HSP12.3.0	2020-12-01	Mai	Mai
16	Improvements and bug fixes with HSP12.4.0	2021-03-11	Mai	Mai
17	Improvements and bug fixes with HSP12.5.0	2021-06-29	Mai	Mai
18	Improvements and bug fixes with HSP12.6.0	2021-09-16	Nirschl	Nirschl
19	Improvements and bug fixes with HSP13.1.0	2022-06-10	Nirschl	Nirschl
20	Improvements and bug fixes with HSP13.4.0	2023-03-10	Mai	Mai
21	Improvements and bug fixes with HSP13.5.0	2023-06-20	Nirschl	Nirschl
22	Improvements and bug fixes with HSP13.6.0	2023-09-15	Nirschl	Nirschl
23	Improvements and bug fixes with HSP13.7.0	2023-12-05	Mai	Mai
24	Improvements and bug fixes with HSP14.0.0	2024-03-08	Nirschl	Nirschl
25	Improvements and bug fixes with HSP14.1.0	2024-06-11	Nirschl	Nirschl

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1 General Information

1.1 Safety Notice

Calibration activities influence the behavior of the ECU and the systems controlled by the ECU. This may result in unexpected behavior of the vehicle and thus can lead to safety critical situations. Only well trained personnel should be allowed to perform calibration activities.

1.2 System Requirements

The following minimum system prerequisites have to be met:

Required Hardware

- GHz Processor
- GB RAM
- DVD-ROM drive (for installation)
- Network adapter
- Graphics with a resolution of at least 1024x768, 256 MB RAM, 16bit color and DirectX9

Required Operating System

- Windows® 7 SP1 (32 or 64bit*) or higher.
- Windows® 8 (32 / 64 bit*) and Windows® 8.1 (32 / 64 bit*)
- Windows® 10

*) INCA uses the 32bit compatibility mode on a 64-bit operating system.

Required Free Disk Space

- 1 GB (not including the size for user data; absolute min. required, but not recommended)

The following system prerequisites are recommended:

Recommended Hardware

- GHz Quad-Core Processor or equivalent
- 16 GB RAM
- DVD-ROM drive (for installation)
- Network adapter
- Graphics with a resolution of at least 1280 x 1024, 1GB RAM, 32bit color and DirectX9

Recommended Operating System

- Windows® 7 SP1 64bit (INCA uses the 32bit compatibility mode on a 64-bit operating system)

Recommended Free Disk Space

- >10 GB

Recommendation on Performance

- Investigation on performance showed:
 - More Memory improves execution time of repetitive operations
 - SSD hard disks improve the file access times

1.3 Restrictions

WINDOWS® 95b, WINDOWS® NT, WINDOWS® 2000, WINDOWS® 98SE, and WINDOWS® XP are not supported

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1.4 Miscellaneous

To ensure the highest data throughput from the BR_XETK device up to the PC system the following recommendations should be considered:

- Set power save mode to the highest level
- Disable virus scan
- Use network adapter for ETAS application only
- Update network adapter drivers

2 Version Syntax and Tool-Chain Information

2.1 Version-Syntax of the BR_XETK-S3.0A

The BR_XETK-S3.0A hardware version information is located on the product sticker and can be read out of the XETK using the firmware update tool HSP or XETK Configuration Tool.

Hardware State Syntax: **abb/cc**

Description (modification details refer chapter 5)

a	PCB Version (A=V1.0, B=V1.1, C=V1.2, ...)
bb	PCB Hardware State (010, 011, 012, ...)
cc	PCB Population Variant (00, 01, 02, ...)

The BR_XETK-S3.0A Firmware version information can be read out of the XETK using the firmware update tool HSP or XETK Configuration Tool. It is not printed onto a XETK sticker.

Firmware-Version Syntax: **aaa.bbb.ccccc**

Description (modification details refer chapter 5)

aaa	Major Release (0...255)
bbb	Minor Release (0...255)
cccc	Revision/Patch (0...65535)

Firmware Packages:

HDC Work	aaa.bbb.ccccc
Firmware Work	aaa.bbb.ccccc
HDC Rescue	aaa.bbb.ccccc
Firmware Rescue	aaa.bbb.ccccc

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2.2 Version information of the tool-chain components

To get this XETK running with the other components of the tool-chain please make sure that the version mentioned below or a newer one is used. If your software-, firmware- or hardware version is older, please update it.

If you have any problems to get this XETK running, please contact our local customer support or sales representative.

Updates or refreshes can be downloaded from the ETAS homepage:

<http://de.etasgroup.com>

<http://en.etasgroup.com>

2.3 Software and microcontroller support

Microcontroller	Method	HSP	INCA	ETK Tools	ASCET-RP	INTECRIO
TC23x-PD	Distab17	V11.10.0	V7.2.10	V4.1.12	V6.4	V4.6
TC26x-ED	Distab17	V11.6.0	V7.2.6	V4.1.7	V6.4	V4.6
	Trace to RAM	V11.6.0	V7.2.6	V4.1.7	V6.4	V4.6
TC26x-PD	Distab17	V11.8.0	V7.2.8	V4.1.9	V6.4	V4.6
TC27x-ED	Distab17	V11.5.1	V7.2.5	V4.1.6	V6.4	V4.6
	Trace to RAM	V11.6.0	V7.2.6	V4.1.7	V6.4	V4.6
TC27x-PD	Distab17	V11.8.0	V7.2.8	V4.1.9	V6.4	V4.6
TC29x-ED	Distab17	V11.5.1	V7.2.5	V4.1.6	V6.4	V4.6
	Trace to RAM	V11.6.0	V7.2.6	V4.1.7	V6.4	V4.6
TC29x-PD	Distab17	V11.8.0	V7.2.8	V4.1.9	V6.4	V4.6
TC36x-PD	Distab17	V11.15.0	V7.2.15	V4.1.16	V6.4	V4.6
TC37x-PD	Distab17	V11.15.0	V7.2.15	V4.1.16	V6.4	V4.6
TC37x-ED	Distab17	V11.12.0	V7.2.12	V4.1.13	V6.4	V4.6
	Trace to RAM	V11.12.0	V7.2.12	V4.1.13	V6.4	V4.6
TC37x-XX	Distab17	V12.3.0	V7.3.4	V4.2.3	V6.4	V4.6
TC38x-PD	Distab17	V11.8.0	V7.2.8	V4.1.9	V6.4	V4.6
TC39x-ED (A-Step)	Distab17	V11.5.1	V7.2.5	V4.1.6	V6.4	V4.6
TC39x-ED (B-Step)	Distab17	V11.8.0	V7.2.8	V4.1.9	V6.4	V4.6
TC39x-ED (B-Step)	Trace to RAM	V11.9.0	V7.2.9	V4.1.10	V6.4	V4.6
TC39x-XX	Distab17	V12.3.0	V7.3.4	V4.2.3	V6.4	V4.6

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3 What's New - Release Notes

This chapter lists the main improvements compared to a previous shipped ETK product. Additionally, a detailed list of already known issues can be found here.

3.1 New or Enhanced Functions

3.1.1 In INCA 7.5.1 and HSP 14.1.0

Issue Identifier	Description
ETKX-4674 #747134	Allow two DAQ-Lists per event for static Distab17
ETKUS-3483	Support 50MHz 3Pin DAP

3.1.2 In INCA 7.5.0 and HSP 14.0.0

Issue Identifier	Description
#743317	XETK disconnection leads to OMD reconfiguration when bypass is active
ETKX-4547	Fix problems with XETK debug log
ETKX-4591	Fix and Improve behavior with multiple XCP connections
ETKX-4389	Improve XCP error message on failures of initializing overlay RAM
ETKX-3797	XETK Watchdog Counter as monitoring variable
ETKX-4053	Support for Semantic Versioning
ETKX-4464	Integrate XETK Logger Mode incl. packed mode
ETKUS-3257	DRA Support for TC38x

3.1.3 In INCA 7.4.7 and HSP 13.7.0

Issue Identifier	Description
ETKX-4131	Support CRC32 as checksum algorithm
ETKX-3866	Extension of XCP flash API
#732585	Drive low of DAP data line before switch to receive

3.1.4 In INCA 7.4.6 and HSP 13.6.1

Issue Identifier	Description
#741871	Allow the static configuration of rasters to run successfully in case of dynamic allocation of Distab17 when the distabreadvarcount is not defined in configuration

3.1.5 In INCA 7.4.6 and HSP 13.6.0

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Issue Identifier	Description
ETKX-3237	Support für Distab Resource Distribution by XETK

3.1.6 In INCA 7.4.5 and HSP 13.5.0

Issue Identifier	Description
ETKX-2912	Extend XCP command DBG_GET_VENDOR_INFO
#725336	Invalid ECU memory access may occur when reading ECU info pattern

3.1.7 In INCA 7.4.4 and HSP 13.4.0

Issue Identifier	Description
ETKX-2998	Support Watchdog Disable via SPI
ETKX-2557	Support ECU Info Mailbox incl. Extension of GET_ID (include EPK, A2I file name if provided by ECU)
ETKX-3393	Improve out-of-memory behavior

3.1.8 In INCA 7.4.1 and HSP 13.1.0

Issue Identifier	Description
ETKX-601	XETK returns XCP V1.5 is supported
ETKX-2202	XETK sends measurable events in case of DAQ or Trace overload
ETKX-2397	Support XCP Event Channel detection for INTECRIO
ETKF-1304	Drop support for TC27x A-step
ETKX-2138	XCP SW Debug API V1.1 is supported
ETKX-1650	Counters for ECU resets are available as monitoring variables
#682863	Fix formatting of trace overflow event
#684489	XCP CONNECT does not reset XCP TX counter when connection already exists

3.1.9 In INCA 7.3.6 and HSP 12.6.0

Issue Identifier	Description
#664102	Fix Start on working page after XETK Standby
#663507	XETK doesn't respond to unknown XCP user commands
#668138	Fix Time Correlation Properties Hang up

3.1.10 In INCA 7.3.5, HSP12.5.0 and XCT 4.2.5

Issue Identifier	Description
ETKPRG-974	Release of additional page switching method "DRA" (Direct Register Access). Available initially for TC39x-ED. Requires HSP12.5.0 and INCA 7.3.5 or newer.

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3.1.11 In INCA 7.3.4 and HSP 12.4.0

Issue Identifier	Description
ETKX-1063	Implement a standby configuration storage for XETK
ETKX-915	Enable TCP window scaling in the LWIP stack
ETKX-169	Improve monitor variables
ETKX-1027	Report measurement status in GET_STATE with key 0x16 (Measurement Status)
# 650642	XETK: TCP retransmit might be suboptimal under conditions with ethernet errors

3.1.12 In INCA 7.3.3 and HSP 12.3.0

Issue Identifier	Description
ETKPRG-824	Support TC39x-XX and TC37x-XX
ETKX-887	Improve error handling in case of too many signals
# 645177	Loss of OMD / Working page after XETK standby
# 643709	TC39x EMEM DAP access timeout
# 638266	Error propagation in XCP processor occasionally not done right
# 642381	Out of Memory Error not propagating to user

3.1.13 In INCA 7.3.0 and HSP 12.0.0

Issue Identifier	Description
# 626970	ECU traps due to EE change when ECU is off
# 629990	ECU traps due to switching to new PC without stopping the measurement before
ETKPRG-621	Support TC33xPD
ETKPRG-651	Concurrent XCP debugging and PROF flashing

3.1.14 In INCA 7.2.15 and HSP 11.15.0

Issue Identifier	Description
ETKPRG-517 ETKPRG-539	Support of TC36x-PD and TC37x-PD
# 624438	Align XETK Watchdog disable Pin (WGDIS) behavior

3.1.15 In INCA 7.2.14 and HSP 11.14.0

Issue Identifier	Description
# 620289	TimeCorrelationProperties: Epoch of grandmaster's clock reported wrong
# 611515	XETK_Configuration: 5us trigger polling period could result in an inaccessible XETK
# 618275	Standby Current optimized
# 618708	CLOCK_INFO reports wrong time stamp unit for XCP slave clock

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# 619762	Infrequent wrong order of XCP counter between DTO and CTO response
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3.1.16 In INCA 7.2.13 and HSP 11.13.0

Issue Identifier	Description
# 614319	XCP time_correlation_properies SYNC_STATE of grandmaster clock not set properly
# 611369	GET_DAQ_EVENT_INFO should set DAQ_PACKED_MODE supported Bit/Flag if upported
ETKX-161	Support XCP V1.5 Software Debugging API
ETKX-257	Support of XCP time correlation

3.1.17 In INCA 7.2.12 and HSP 11.12.0

Issue Identifier	Description
ETKPRG-158	Support for DAQ packed mode (dynamic DAQ lists)
ETKPRG-227	Support of TC37x-ED
# 602235	Measurement Overflow in case of heavy network load

3.1.18 In INCA 7.2.11 and HSP 11.11.0

Issue Identifier	Description
# 599165	Improve error behaviour in case of heavy trace overload
# 600925	Loss of connection in case of DAQ Overload (XCP counter error)
n/a	Improve DAP performance for Trace2RAM use case
n/a	Speedup start of measurement in case of Trace2Ram use case

3.1.19 In INCA 7.2.10 and HSP 11.10.0

Issue Identifier	Description
# 595893	IP Manager cannot move XETK to different IP subnet when static IP is set
# 598233	Supplementary Distab may overwrite memory in ECU in some cases
# 597559	Improved DAP clock at 160MHz
# 593285	Trace2Ram and concurrent Debugger-Access may lead to sporadic trace errors
# 595737	No error message for wrong trace windows config
# 595643	RasterCheck does not consider correct MCU resources in case of Trace2Ram with TC39xED A and B step
n/a	TC23x-PD supported

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3.1.20 In INCA 7.2.9 and HSP 11.9.0

Issue Identifier	Description
n/a	BR_XETK-S3: Trace2Ram support for TC39xED_B
# 589249	Static calibration with XCT does not work
# 588875	BDR not functioning when ECU has continuous running reset.
# 593528	Unable to avoid cpu mismatch message on TC29x projects
# 592837	Access to ED RAM could fail while concurrently using a debugger by debug api

3.1.21 In INCA 7.2.8 and HSP 11.8.1

Issue Identifier	Description
#589837	BR_XETK-S3 does not initialize DAP - Interface after LBIST

3.1.22 In INCA 7.2.8 and HSP 11.8.0

Issue Identifier	Description
#588791	Calibration not working on some PD devices with (invalid calibration handle)
#584009	Support XCT configured trace rasters with SDISTAB
#585349	Wrong measurement values after DAP errors
n/a	Improve Wait/Ready Pattern implementation (MC-/Rp-WaitBit only set if measurement running)
n/a	Support ExcludeFromTrace
n/a	XCP Event if CPU type is not matching A2L file
n/a	Support TC39xED_B and TC38xx (without trace)

3.1.23 In INCA 7.2.7 and HSP 11.7.0

Issue Identifier	Description
n/a	Added support for LERTv3
#580921, #580922	Improved Rastercheck for Trace2Ram
#576028	Lua plugin not unloaded on loading a new cpu description file
#580544	Possible crash or exception during stop of trace measurement

3.1.24 In INCA 7.2.6 and HSP 11.6.0

Issue Identifier	Description
n/a	Support of PTP / IEEE1588 time synchronization (slave only)
n/a	Support for 3-PIN DAP (wide mode DAP) and 160MHz DAP

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n/a	Trace to RAM support for TC26x-ED, TC27x-ED and TC29x-ED
n/a	Improved stability of XCP debugging feature
#575539	Lauterbach Trace32 connected via XCP and INCA measurement in parallel leads to watchdog reset of XETK
#556140	Monitor Variables are going spuriously to 0
#57186	Lost connection after ECU shutdown

3.1.25 In INCA 7.2.5 and HSP 11.5.1

Issue Identifier	Description
n/a	Initial version, support of BR_XETK-S3
#571227	Timer Measurement over ECU reset is not reliable.

3.2 Known issues

3.2.1 In INCA 7.2.8 and HSP V11.8.0

Issue Identifier	Description
#589249	XETK: static calibration does not work (third party use case)

3.2.2 In INCA 7.2.7 and HSP V11.7.0

Issue Identifier	Description
#581311 & #581332	For Fine Grained Trace, Infineon Errata MCDS_TC.059 has to be considered for TC2xx ECU Software

3.2.3 In INCA 7.2.6 and HSP V11.6.0

Issue Identifier	Description
570287	ES720 does not work with BR_XETK-S3

4 Product Variants

The BR_XETK-S3.0A can be purchased in one variant. For details refer to the user guide.

5 Hardware Modifications

5.1 General remarks to this chapter

Hardware issues or obsolete parts can make it necessary to modify the population of the BR_XETK. The first released version and current version are listed below. For the version syntax please refer to chapter 2.1.

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5.2 First delivered version

The hardware state **C013/01** is the first delivered version.

5.3 Current delivery condition

The hardware state **E010/01** will be delivered with all new shipments.

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6 Firmware Modifications

6.1 General remarks to this chapter

The programmable logic code within the BR_XETK-S3.0A is stored onto programmable logic devices (FPGA, Firmware). The first released version and current version are listed below. For the version syntax please refer to chapter 2.1.

6.2 First delivered version

FPGA Work	1.1.16
Firmware Work	1.1.9
FPGA Rescue	1.1.9
Firmware Rescue	1.1.8

6.3 Current delivery condition

The following firmware versions will be programmed into all BR_XETK-S3.0A shipments:

FPGA Work	1.17.13
Firmware Work	1.25.19
FPGA Rescue	1.1.9
Firmware Rescue	1.1.8

In case of any problems the above mentioned components can be programmed to the XETK by using HSP V14.0.0. This HSP version is similar to the currently delivered XETK products. Newer HSP versions could contain bug fixes and / or new features.

Attention: For updating the XETK with a later version by using the HSP Firmware update tool, all XETK - packages will be updated one after another and this will last a few minutes. Update of FPGA(rescue) and Firmware(rescue) is **not** done in a failsafe manner.

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7 Abbreviations

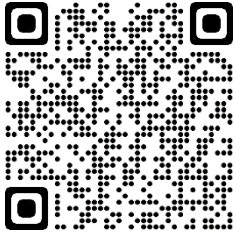
ASCET-RP	Rapid Prototyping Software of ETAS
BR_XETK	Product (emulator test probe)
firmware	Software running on the BR_XETK hardware; may be updated for new features or bug fixes
FPGA	F ield P rogrammable G ate A rray; interface component to the application hardware
Hot-fix	Software bug-fix for a refresh version
HSP	H ardware S ervice P ack; ETAS product which includes the firmware for the complete ETAS hardware, shipped together with INCA but also available as standalone product, download at ETAS homepage possible
INCA	Measurement and Calibration Software of ETAS
INTECRIO	Rapid Prototyping Software of ETAS
MC	M easurement & C alibration
PCB	P rinted C ircuit B oard
RP	R apid P rototyping
SBB	S ervice B ased B ypass
tool-chain	MC hardware (e.g. ES690) and software (e.g. INCA)
XETK Configuration Tool	Configuration Software, in order to configure a (X)ETK / FETK

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8 Contact Information

8.1 Technical Support

For details of your local sales office as well as your local technical support team and product hotlines, take a look at the website: www.etas.com/hotlines



8.2 ETAS Headquarters

ETAS GmbH

Borsigstraße 24

70469 Stuttgart

Germany

Phone: +49 711 3423-0

Fax: +49 711 3423-2106

Internet: www.etas.com